

FIG. 1

160

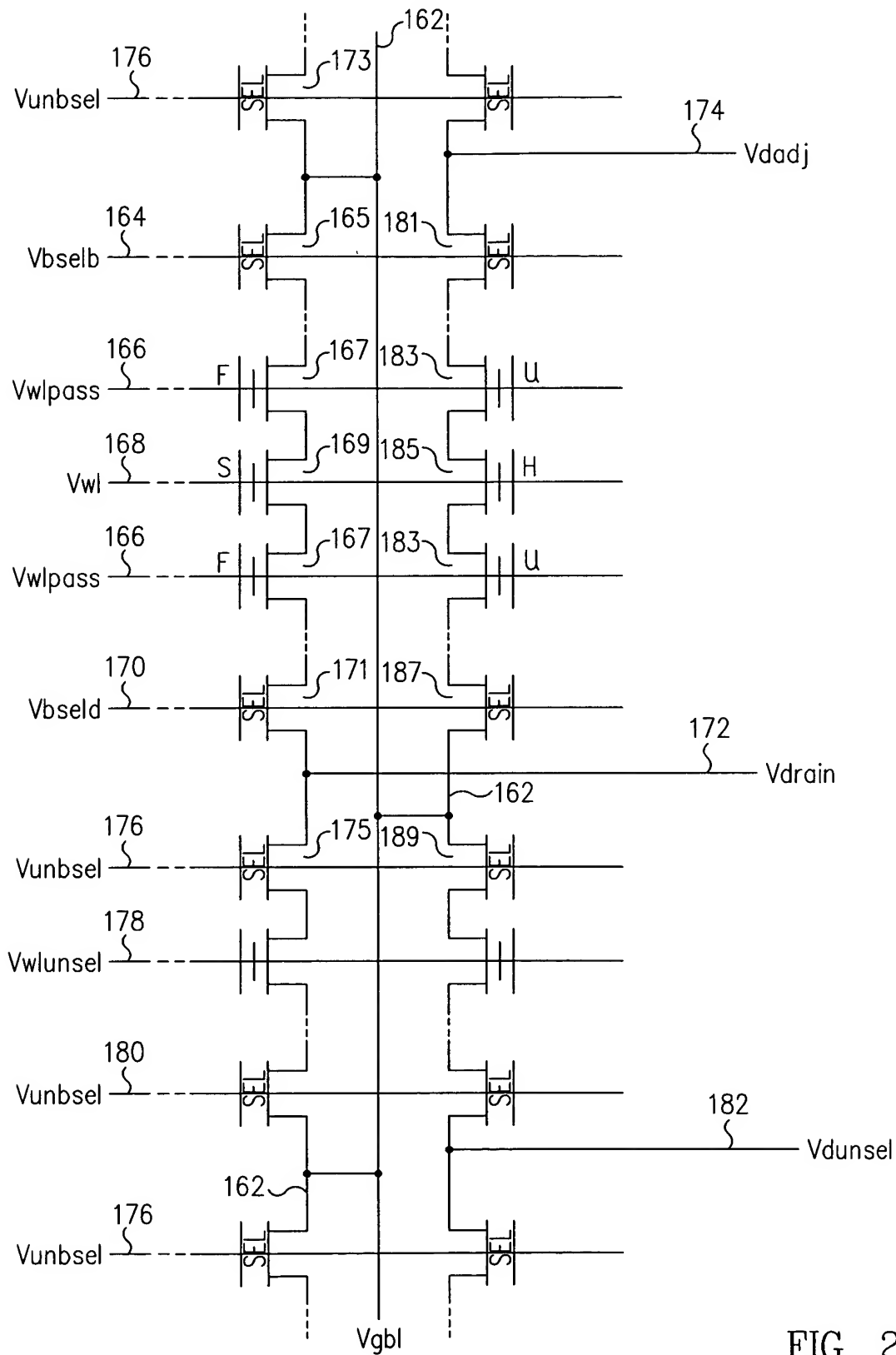


FIG. 2

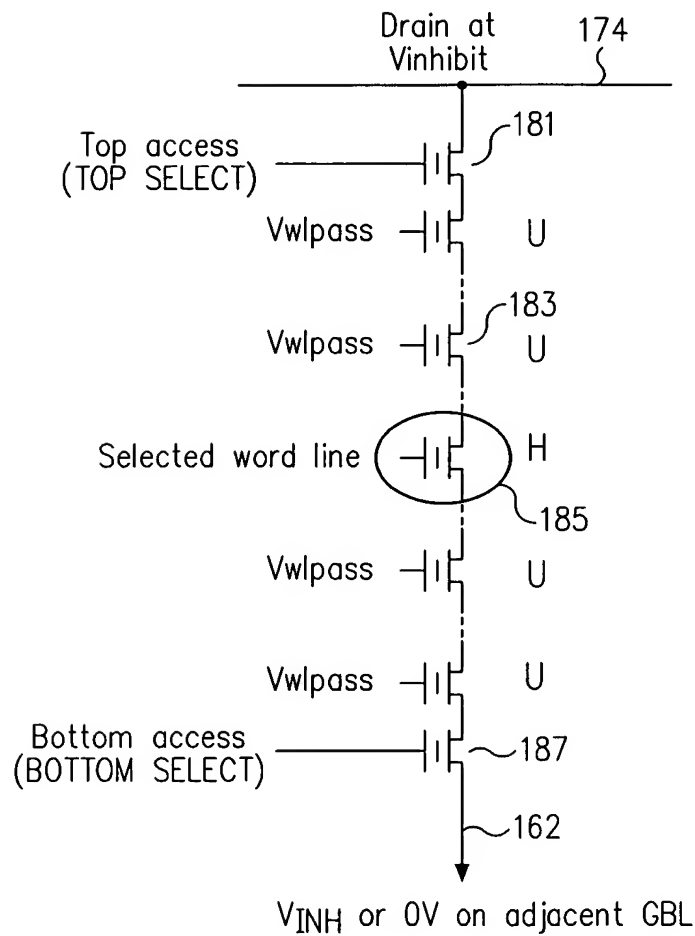


FIG. 3

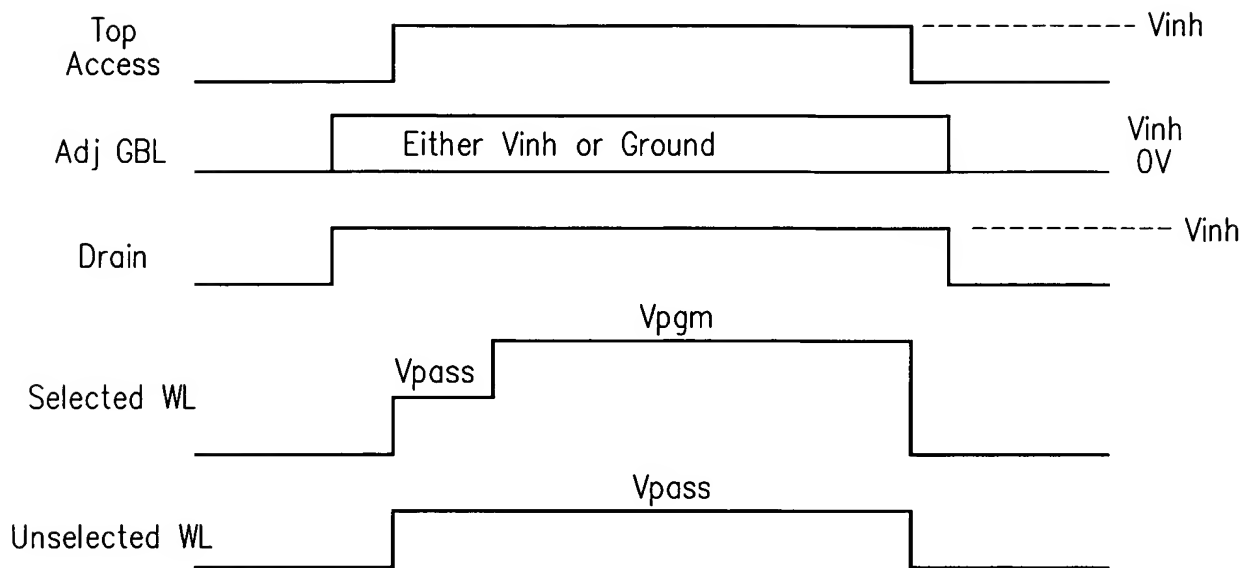


FIG. 4

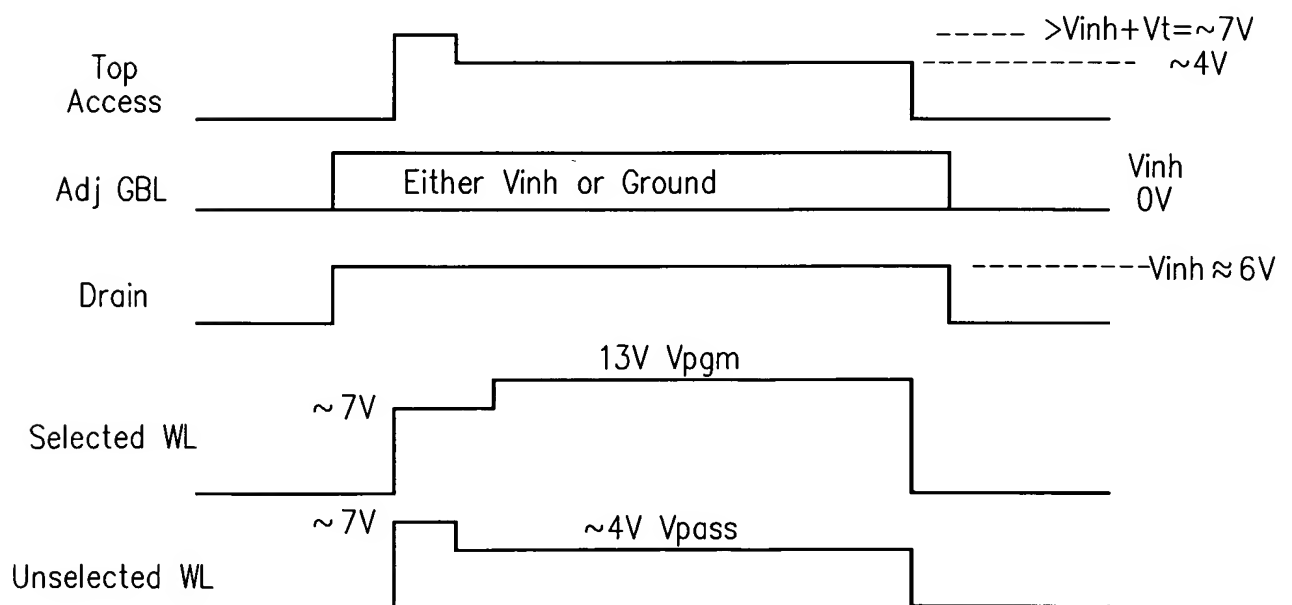


FIG. 5

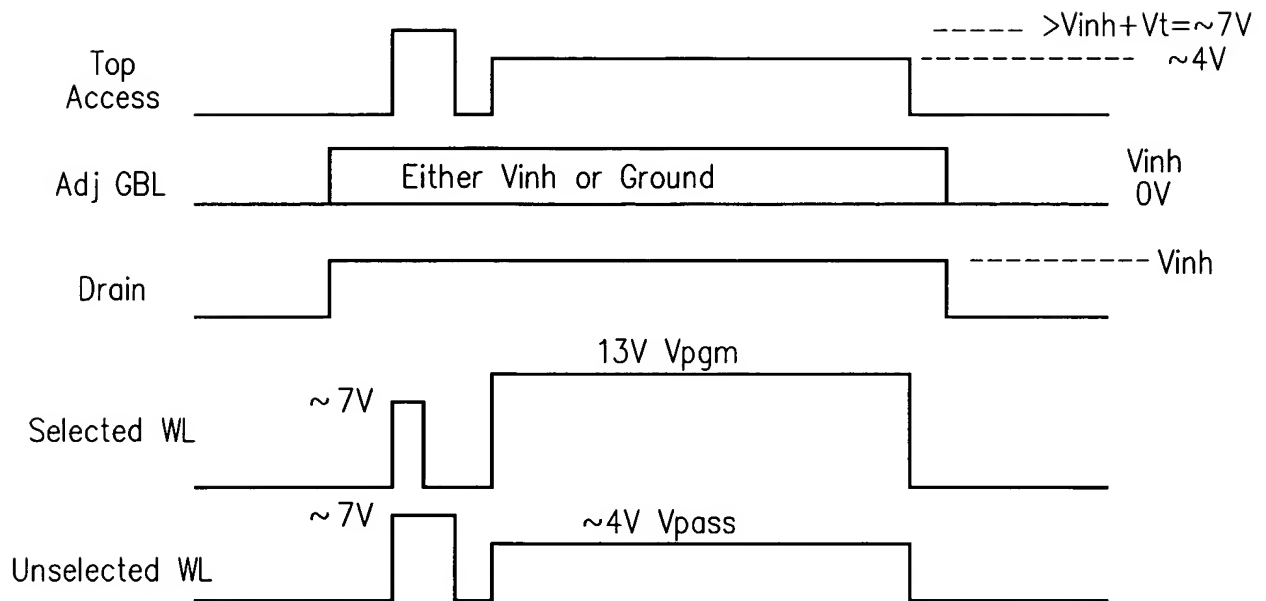


FIG. 6

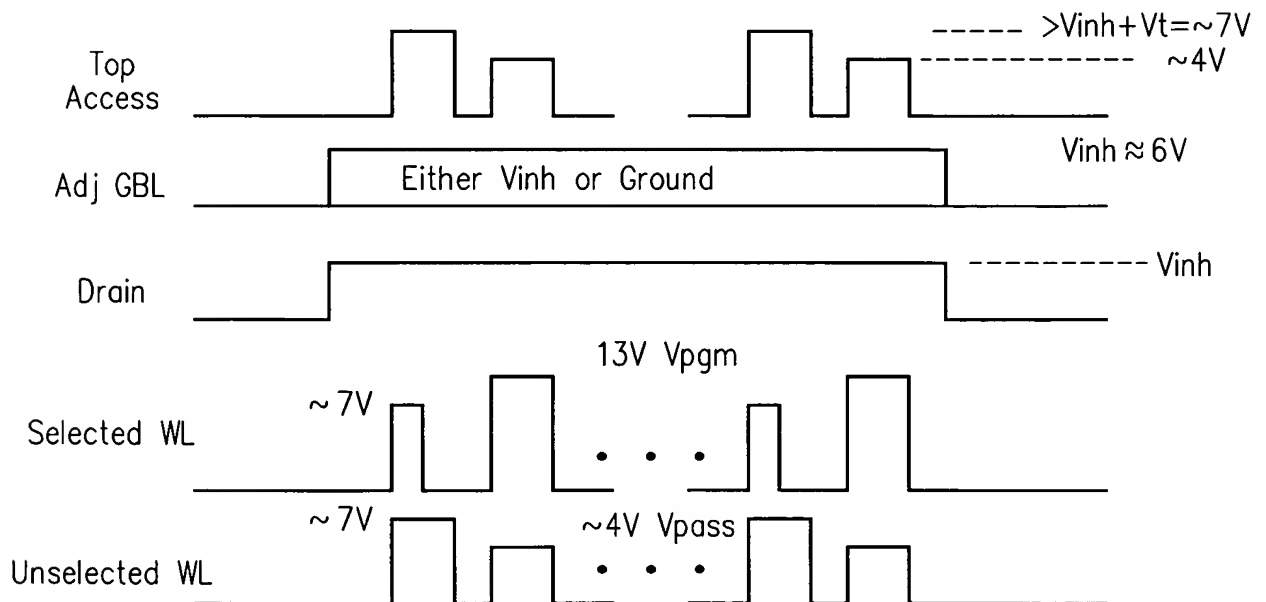


FIG. 7

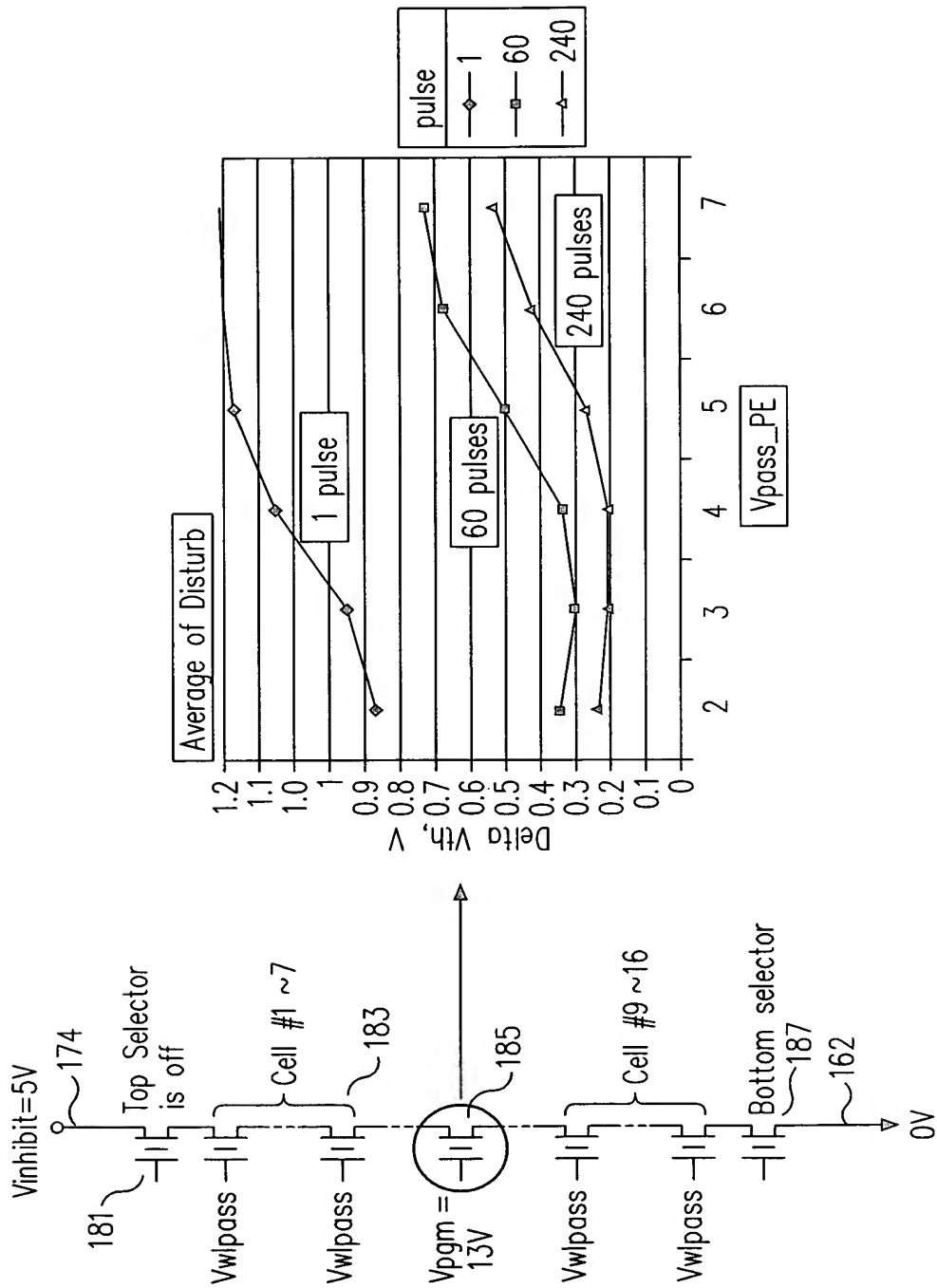


FIG. 8

(ADJACENT STRING
 BEING PROGRAMMED)

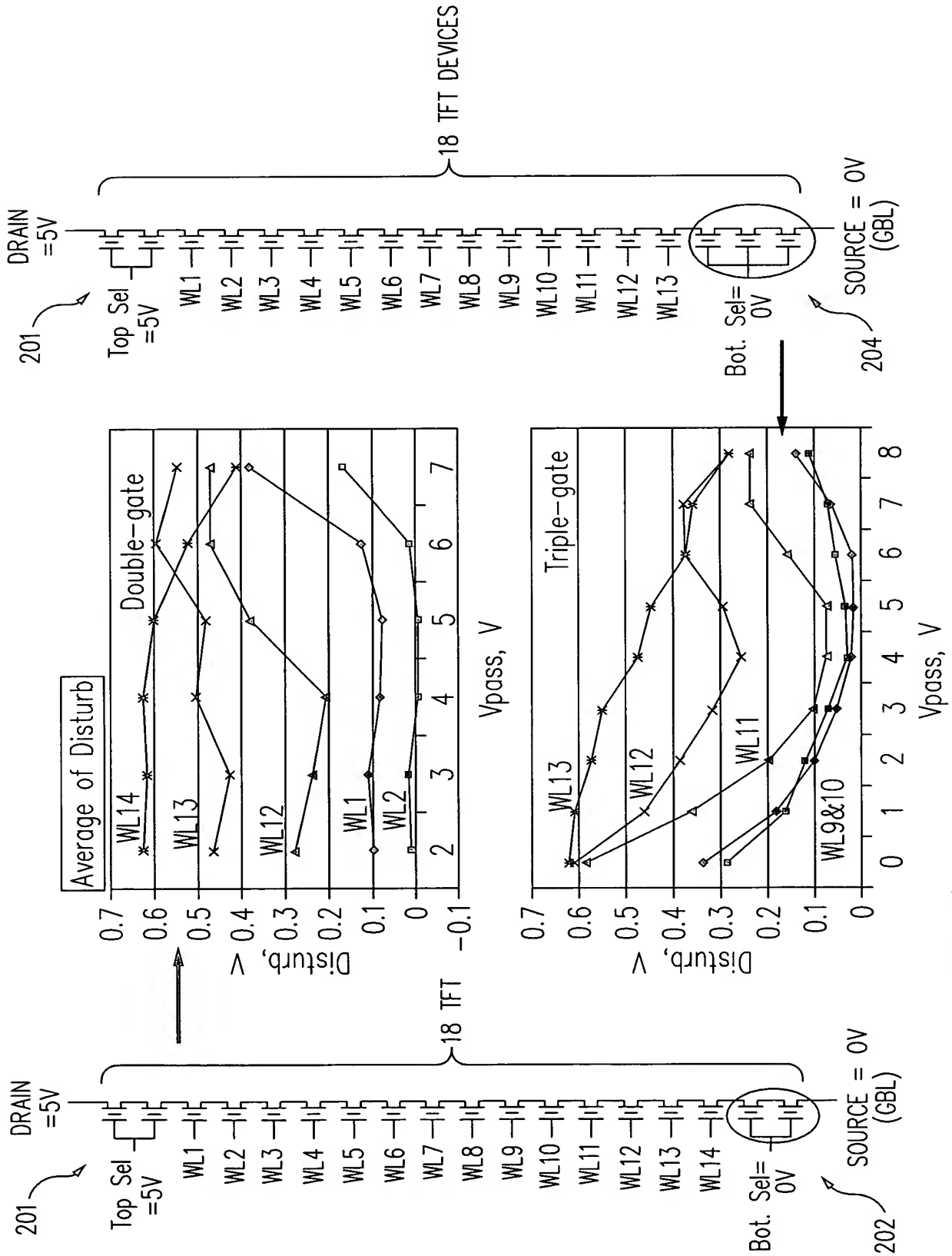


FIG. 9

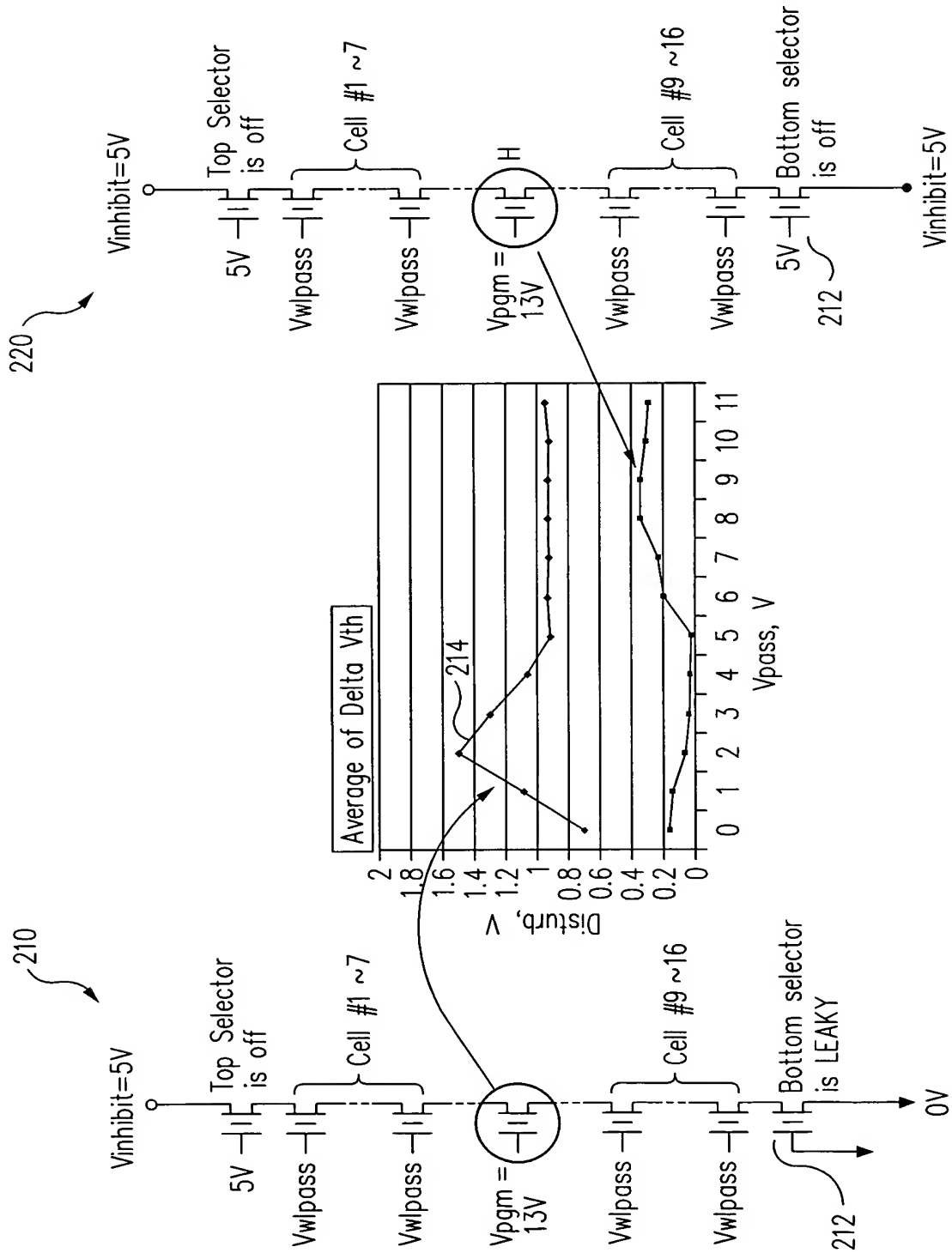


FIG. 10

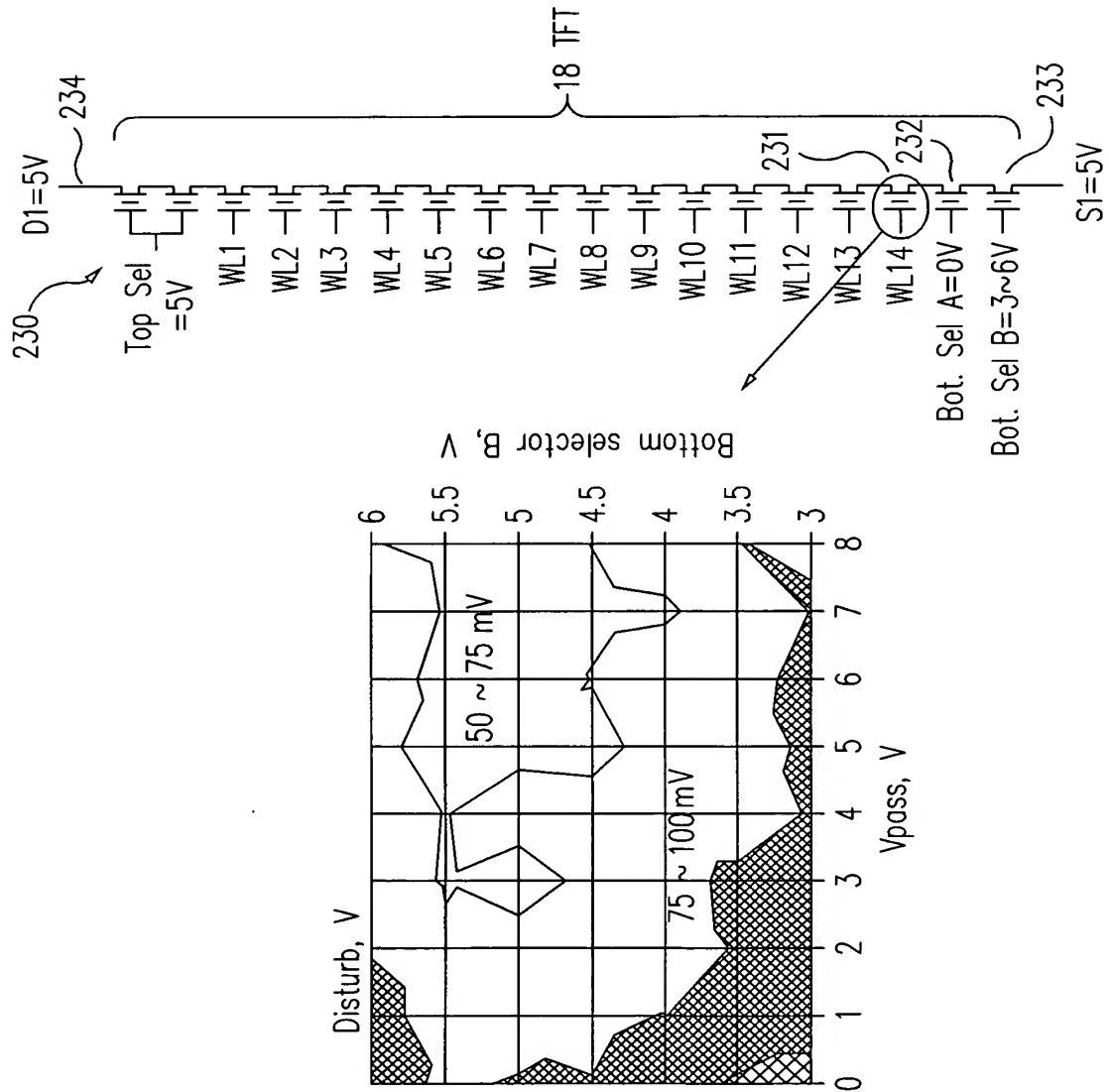


FIG. 11

10/17

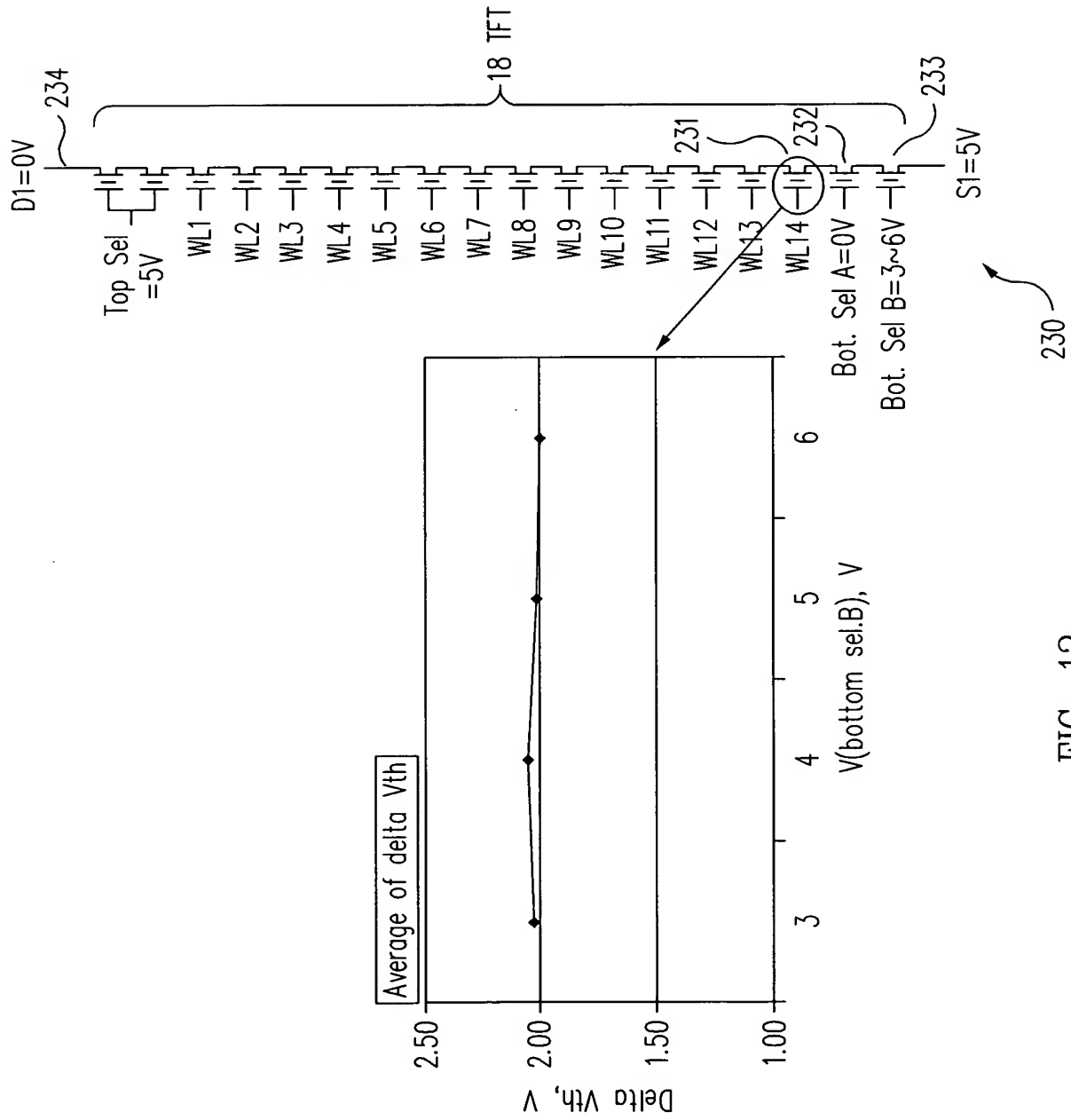
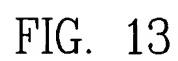


FIG. 12



12/17

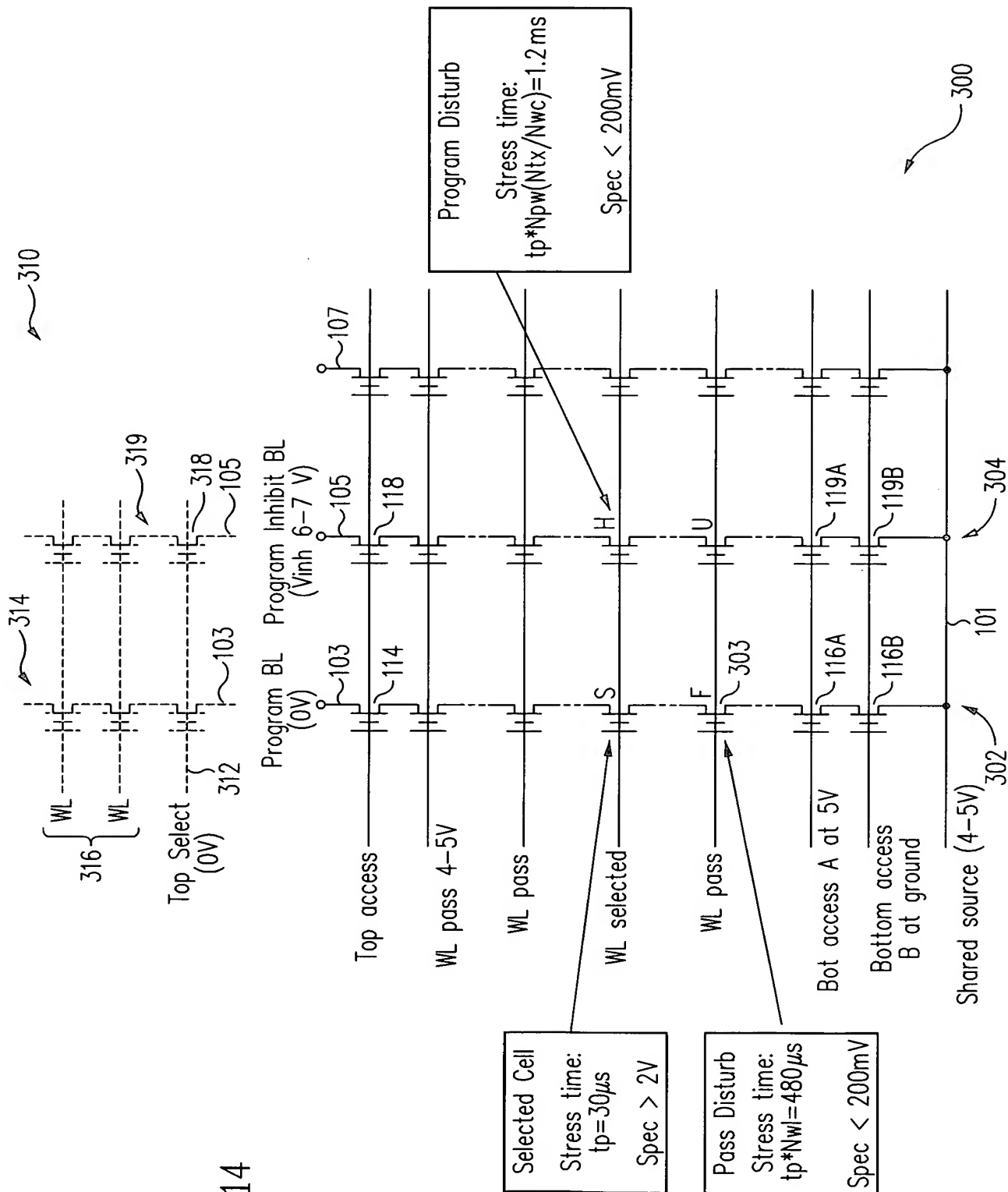


FIG. 14

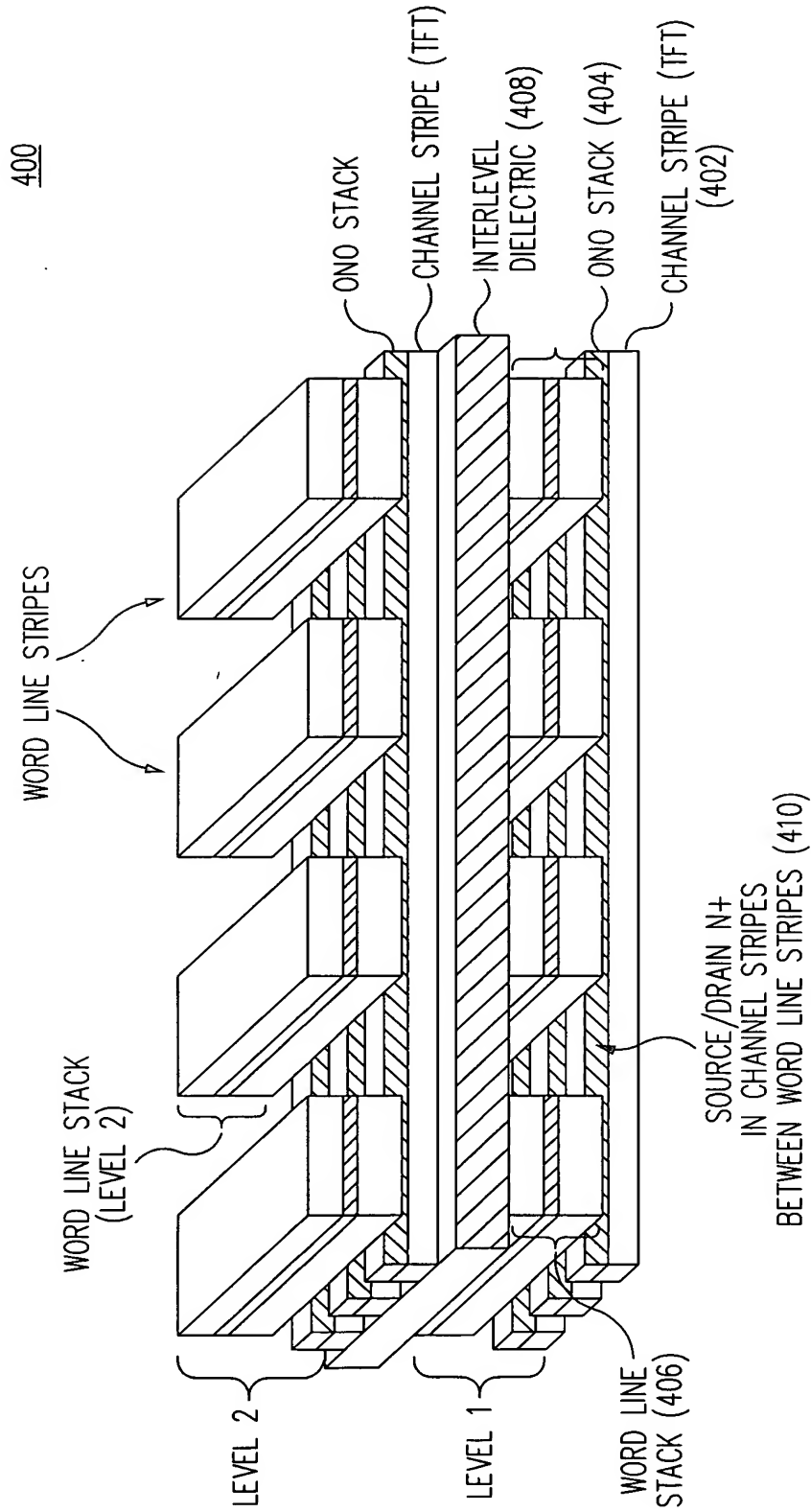


FIG. 15

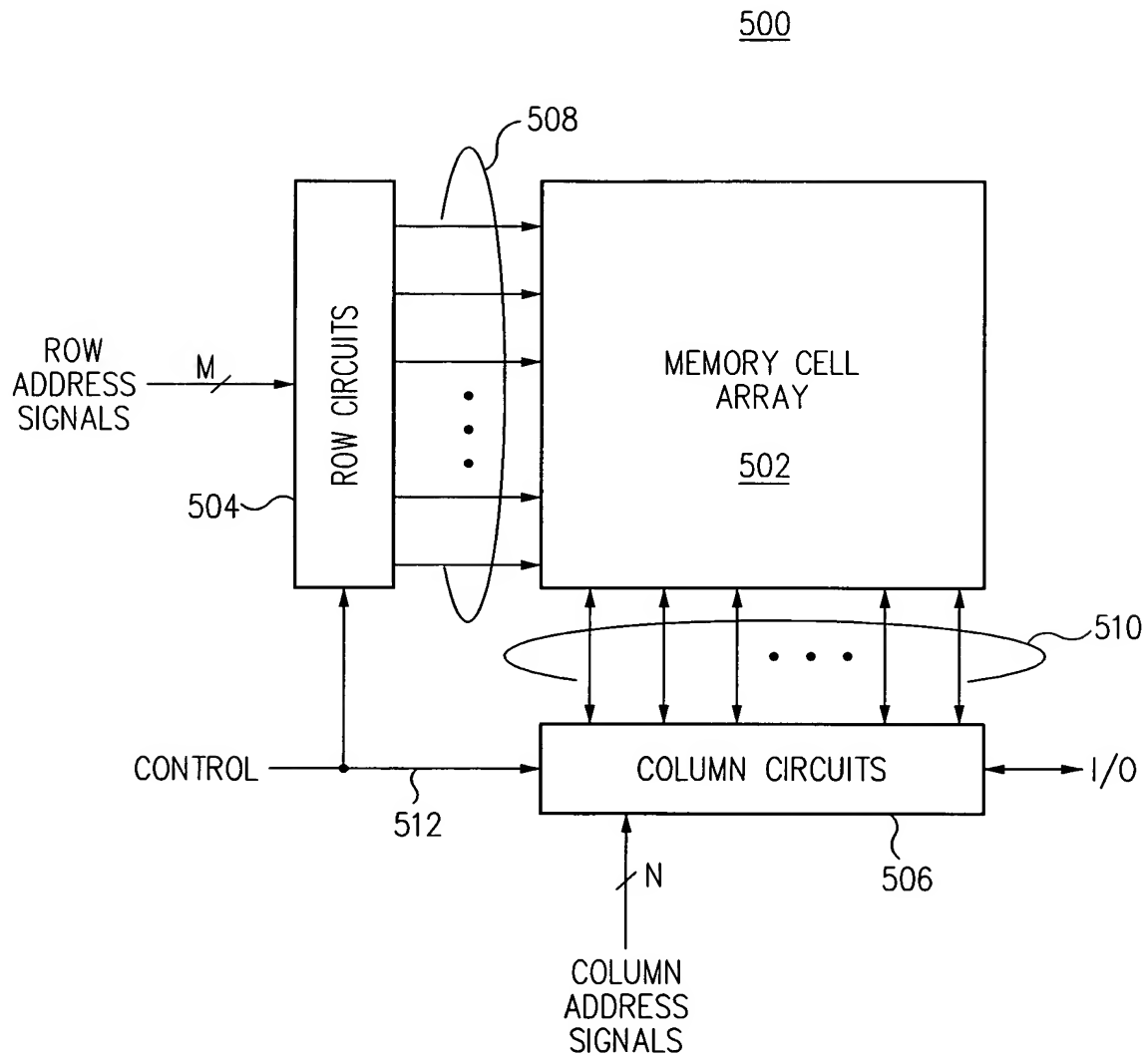


FIG. 16

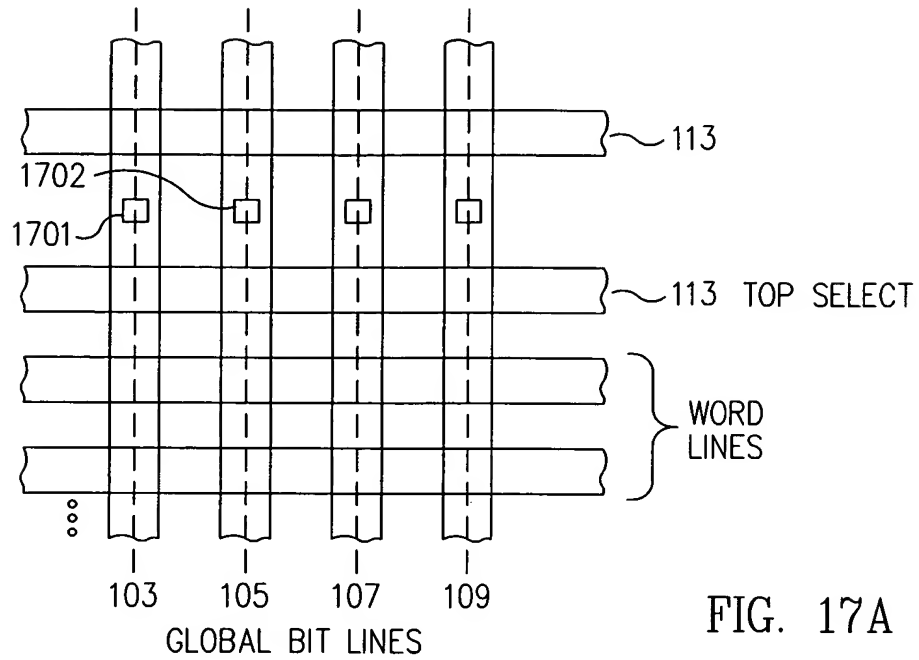


FIG. 17A

GLOBAL BIT LINES 103, 107 ON ONE LAYER
GLOBAL BIT BIT LINES 105, 109 ON 2ND LAYER } FIG. 17B

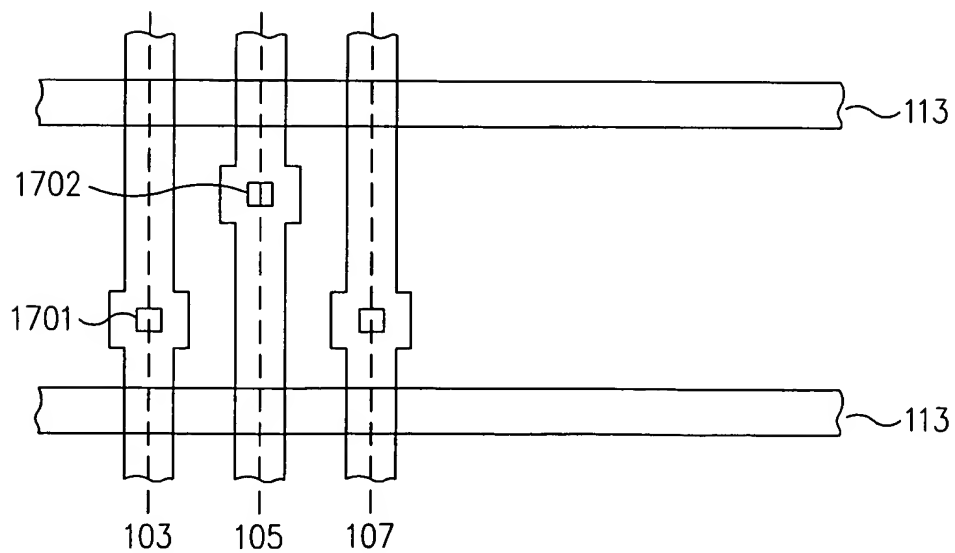


FIG. 17C

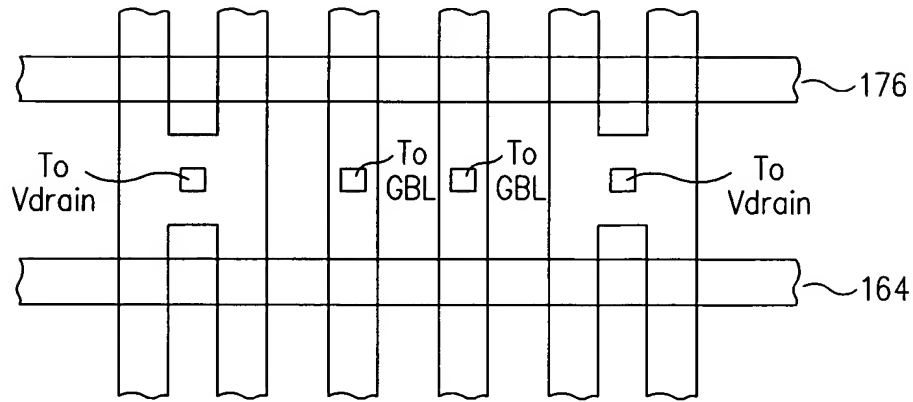


FIG. 17D

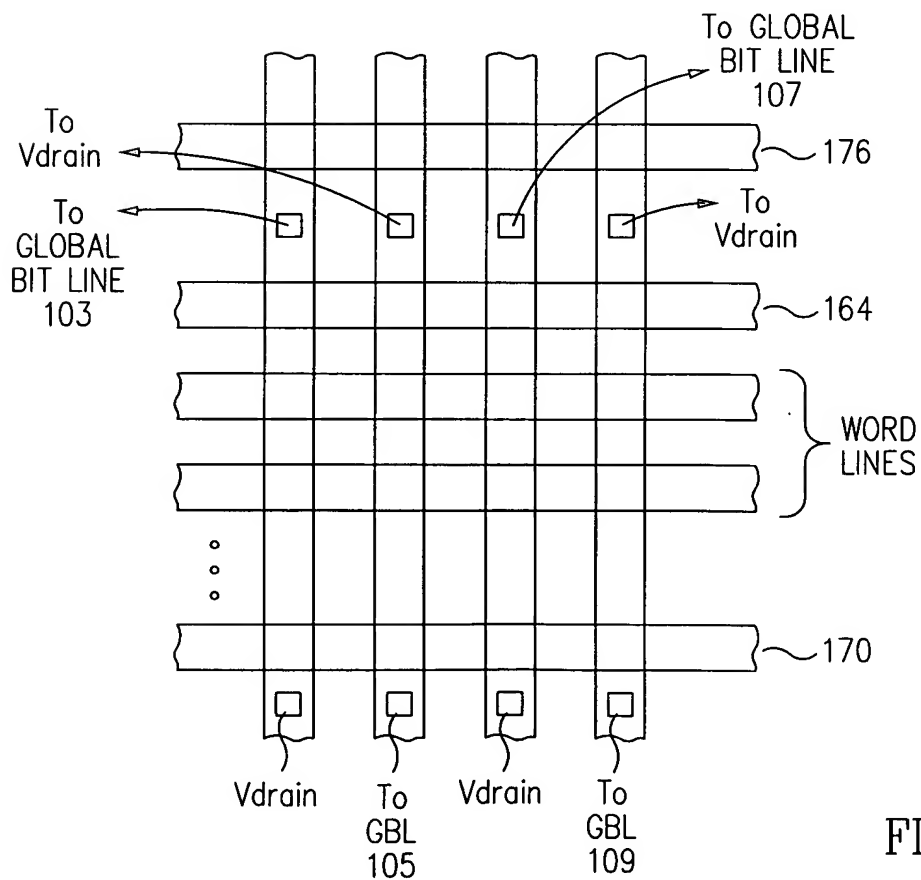


FIG. 17E

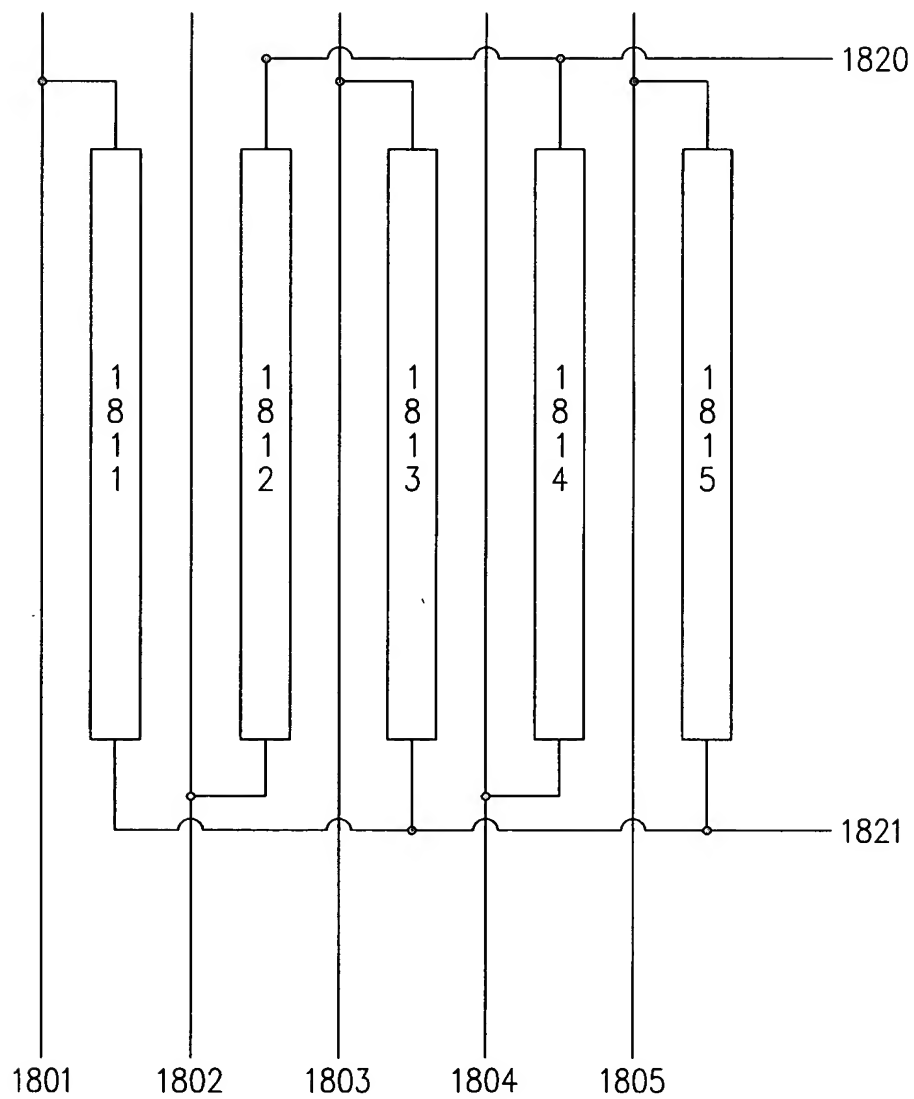


FIG. 18